



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Roth et al.
Serial No.: 09/704,467
Filed : October 31, 2000
Title : EFFICIENT EMULATION INSTRUCTION DISPATCH BASED ON
INSTRUCTION WIDTH

Art Unit : 2183
Examiner : Aimee J. Li
Confirmation No. : 5582
Assignee: Intel Corporation and
Analog Devices, Inc.

MAIL STOP RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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SEP 23 2004

Technology Center 2100

AMENDMENT IN REPLY TO ACTION OF JUNE 16, 2004

Please amend the above-identified application as follows:

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

September 16, 2004
Date of Deposit

Signature

Roxanne Ippolito
Typed or Printed Name of Person Signing Certificate